

AMENDMENTS TO THE ABSTRACT OF THE DISCLOSURE

Please amend the Abstract by rewriting same to read as follows.

A semiconductor apparatus for flexibly and effectively configuring a delay monitor circuit ~~while suppressing without~~ an increase of the in circuit scale ~~to minimum, comprising~~ includes a delay signal generation circuit for switching the configuration of delay element arrays based on first configuration information and second configuration information and propagating a delay element array wherein a pulse is switched, a register group having a first register ~~to be set~~ for the first configuration information and a second register ~~to be set~~ for second configuration information, a selector for outputting to the delay signal generation circuit the first configuration information and second configuration information in accordance with an instruction of a selection signal in a time sharing way, and a control circuit for controlling a power source voltage based on delay information of a delay element array and outputting to the selector a selection signal to select from the first configuration information and second configuration information in a time sharing way.